

**BEST AVAILABLE COPY**

O.I.P.E. SCANNED <i>ggc</i> Q.A. <i>AA</i>	PATENT DATE
---	-------------

**EXAMINER**

Hyun Lee  
Han Nguyen  
Lai Pham

2116

## Method and apparatus for distributing a self-synchronized clock to nodes on a chip

PTO-2040  
12/99

**CROSS REFERENCE(S)**

**SUBCLASS (ONE SUBCLASS PER BLOCK)**

**INTERNATIONAL CLASSIFICATION**

☐ Continued on Issue Slip Inside File Jacket

**CLAIMS ALLOWED**

Print Claim for O.G.

**NOTICE OF ALLOWANCE MAILED**

(Assistant Examiner)

(Date)

**ISSUE FEE**

Amount Due

Date Paid

(Primary Examiner)

\_\_\_\_\_  
(Date)

**ISSUE BATCH NUMBER**

**WARNING:**

The information disclosed herein may be restricted. (Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.)

Form PTO-438A  
(Rev. 6/89)

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM  
(Attached in pocket on right inside flap)

(FACE)